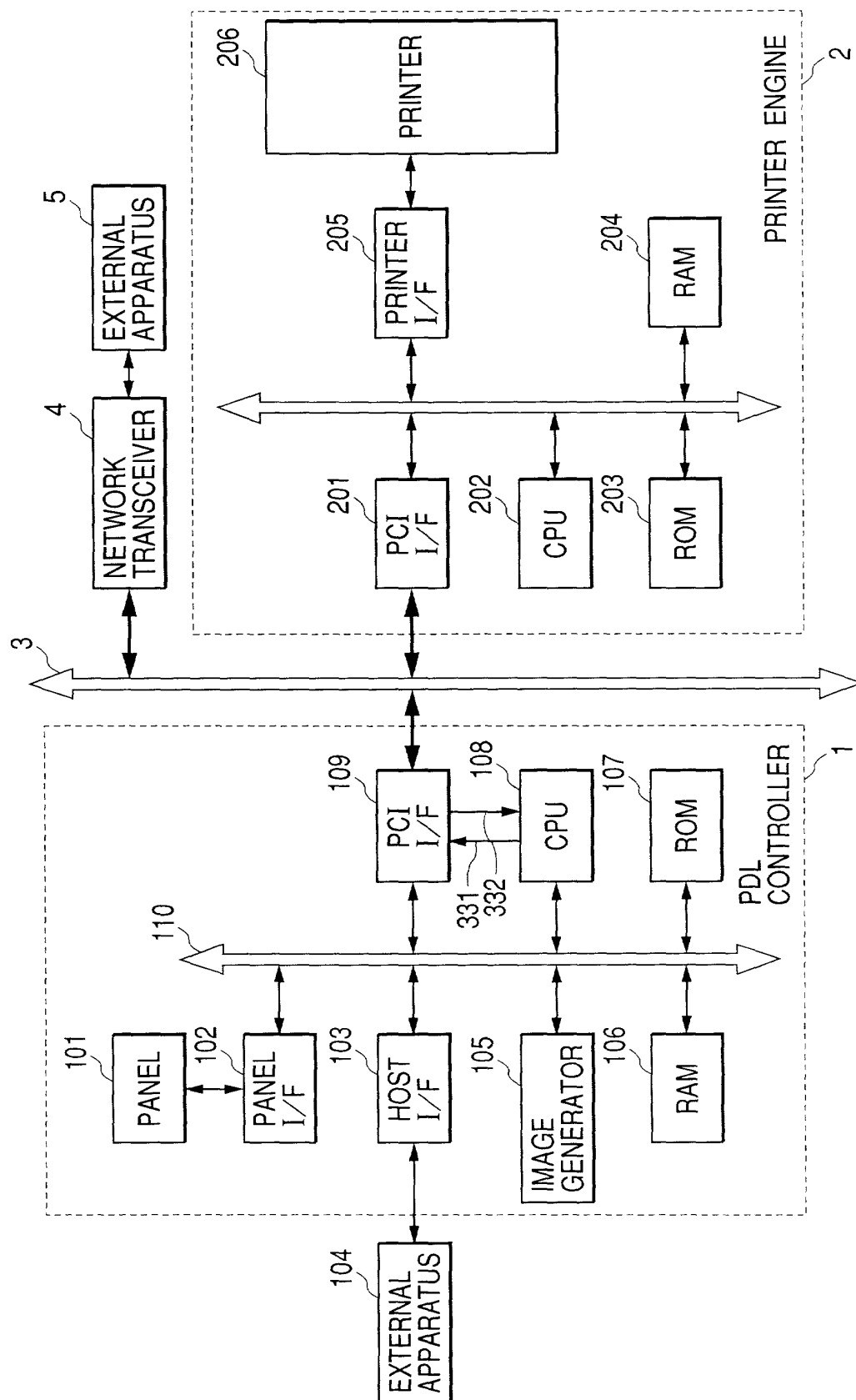
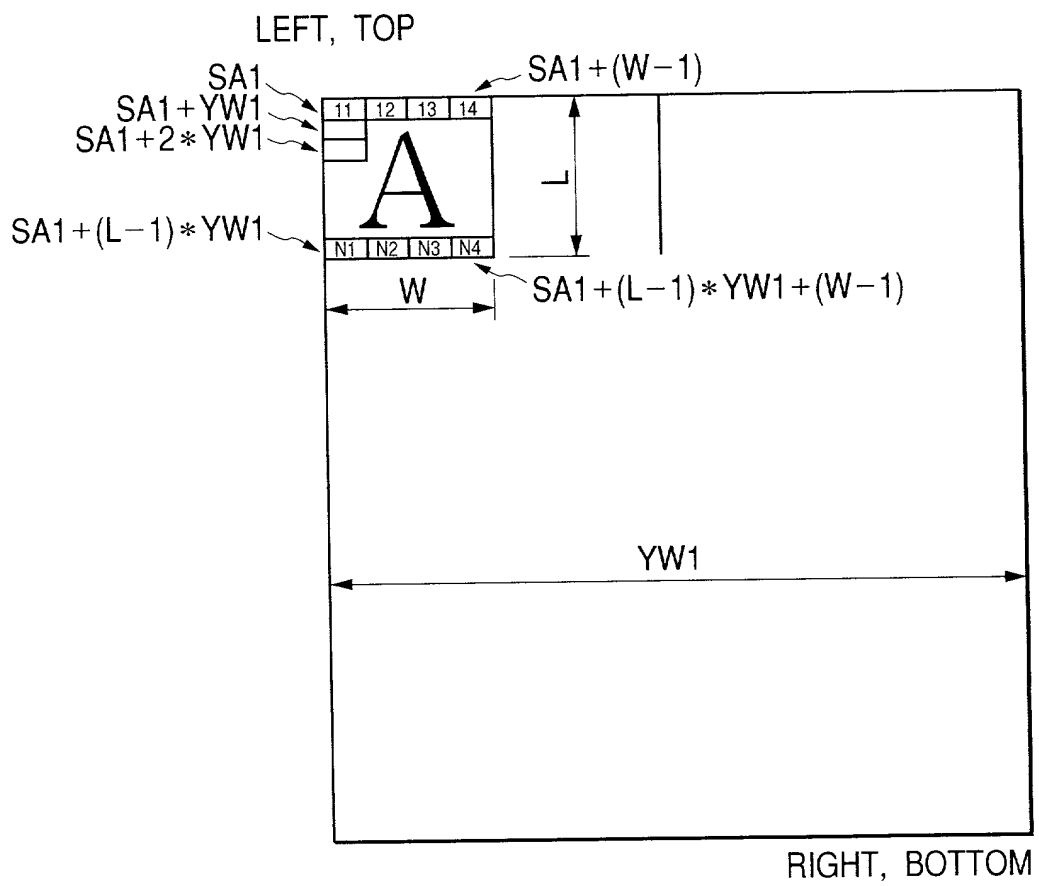


FIG. 1



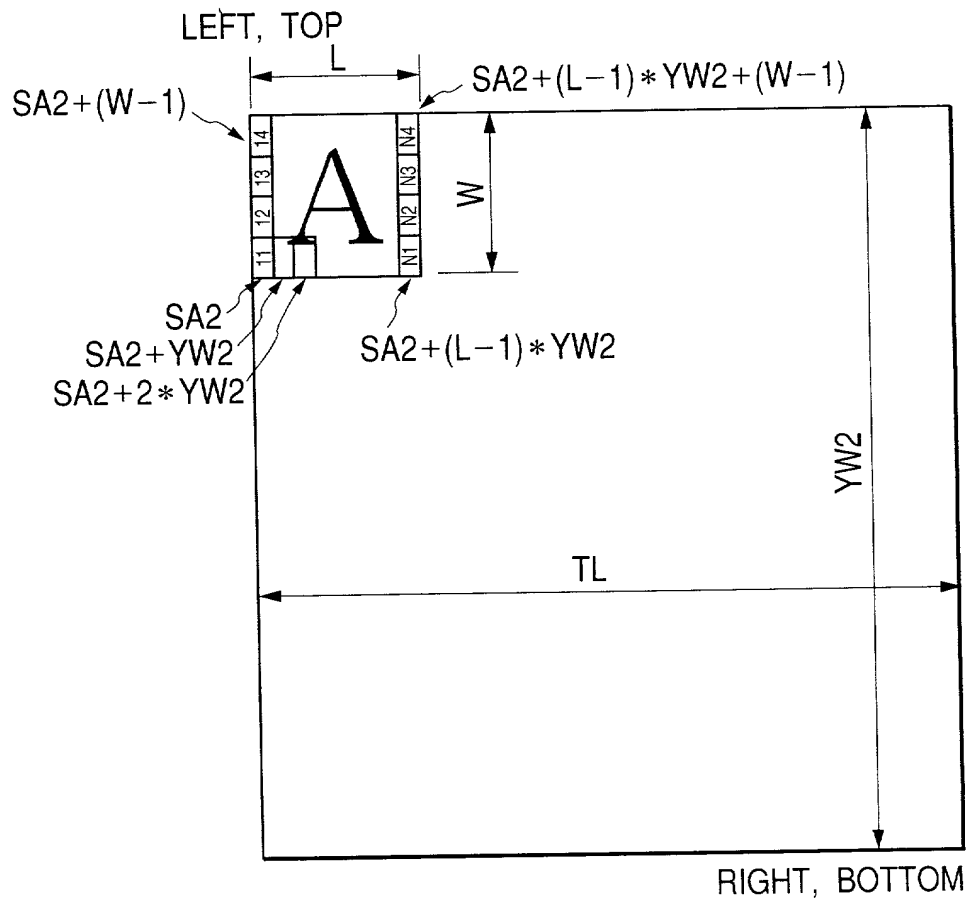


**FIG. 3**



PRINT DATA IN PDL BOARD MEM

**FIG. 4**



PRINT DATA IN ENGINE MEM

100230" 59048860

FIG. 5

CONDITION 1: ((LCS11='1' OR PCS11='1') AND (PCIWR='1' OR PDLWR='1')) SA1<=DT;  
 CONDITION 2: ((LCS12='1' OR PCS12='1') AND (PCIWR='1' OR PDLWR='1')) YW1<=DT;  
 CONDITION 3: ((LCS13='1' OR PCS13='1') AND (PCIWR='1' OR PDLWR='1')) W<=DT;  
 CONDITION 4: ((LCS14='1' OR PCS14='1') AND (PCIWR='1' OR PDLWR='1')) L<=DT;  
 CONDITION 5: ((LCS15='1' OR PCS15='1') AND (PCIWR='1' OR PDLWR='1')) ST<='1';

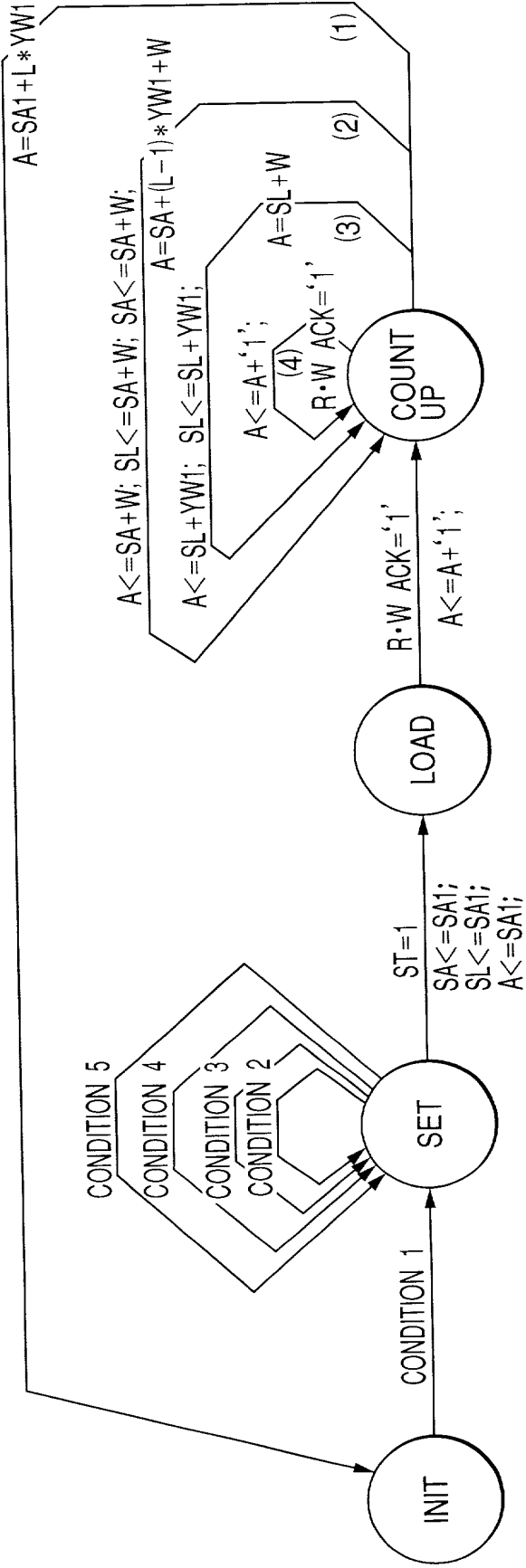
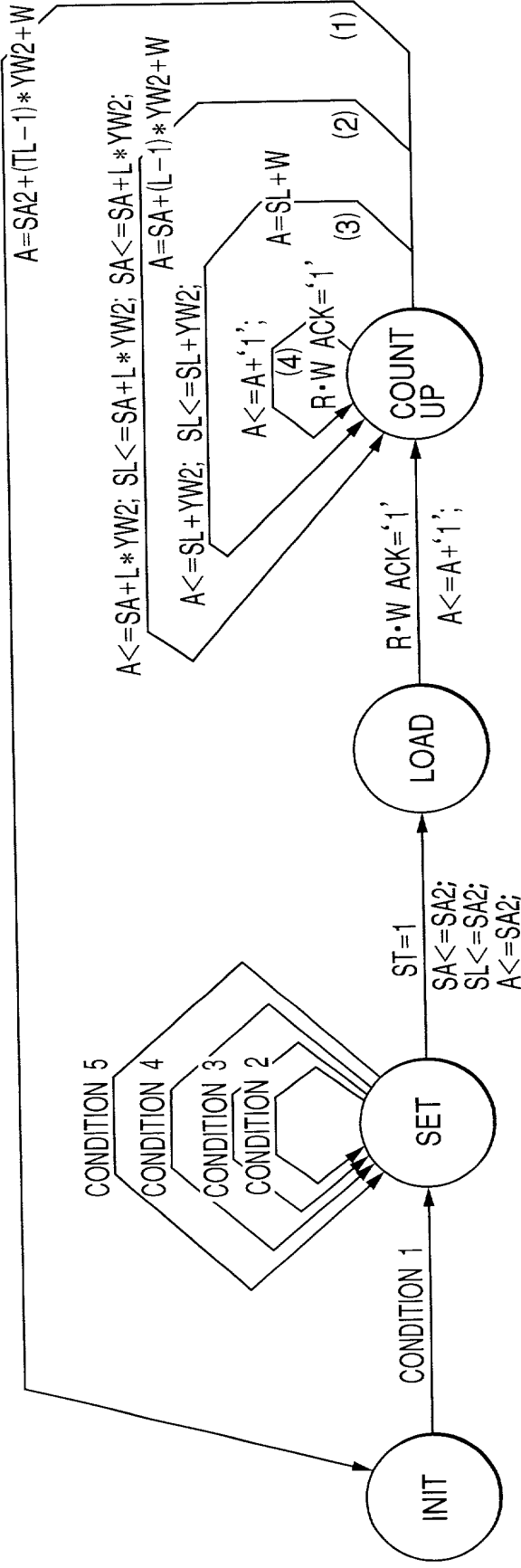
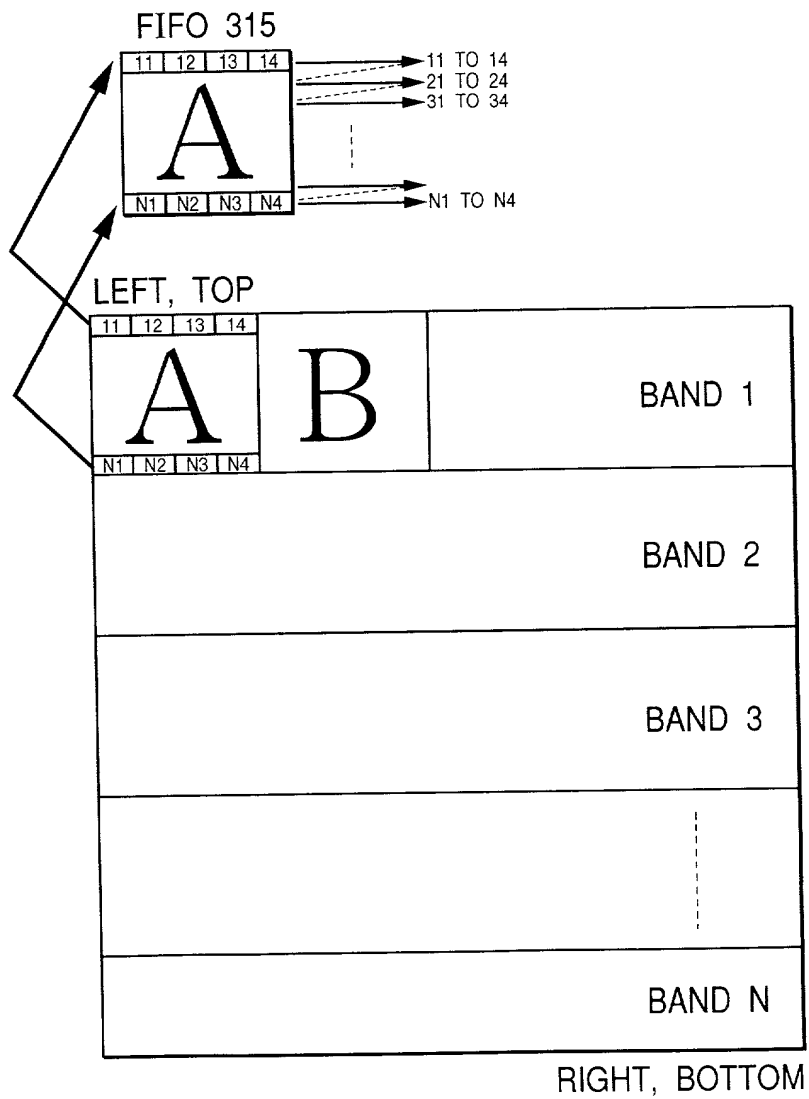


FIG. 6

CONDITION 1: ((LCS21='1' OR PCS21='1') AND (PCIWR='1' OR PDLWR='1')) SA2<=DT;  
 CONDITION 1: ((LCS22='1' OR PCS22='1') AND (PCIWR='1' OR PDLWR='1')) YW2<=DT;  
 CONDITION 1: ((LCS23='1' OR PCS23='1') AND (PCIWR='1' OR PDLWR='1')) W<=DT;  
 CONDITION 1: ((LCS24='1' OR PCS24='1') AND (PCIWR='1' OR PDLWR='1')) L<=DT;  
 CONDITION 1: ((LCS25='1' OR PCS25='1') AND (PCIWR='1' OR PDLWR='1')) TL<=DT; ST<='1';

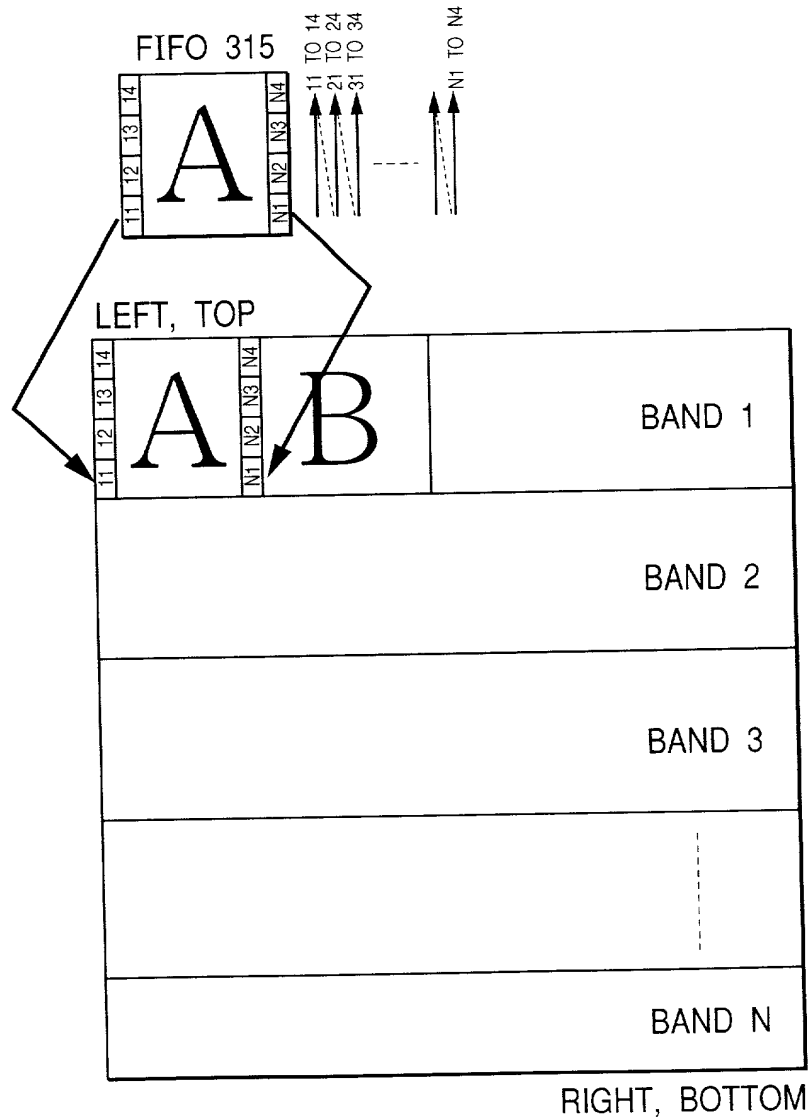


*FIG. 7*



PRINT DATA IN PDL BOARD MEM

FIG. 8

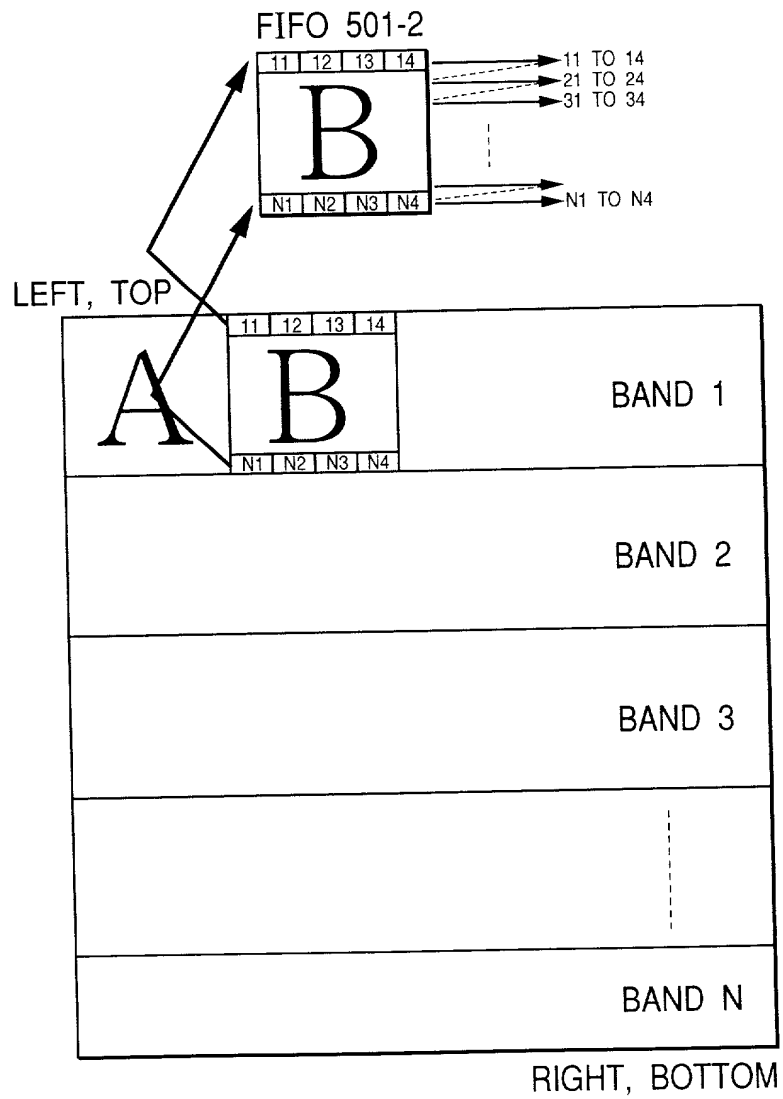


PRINT DATA IN ENGINE MEM



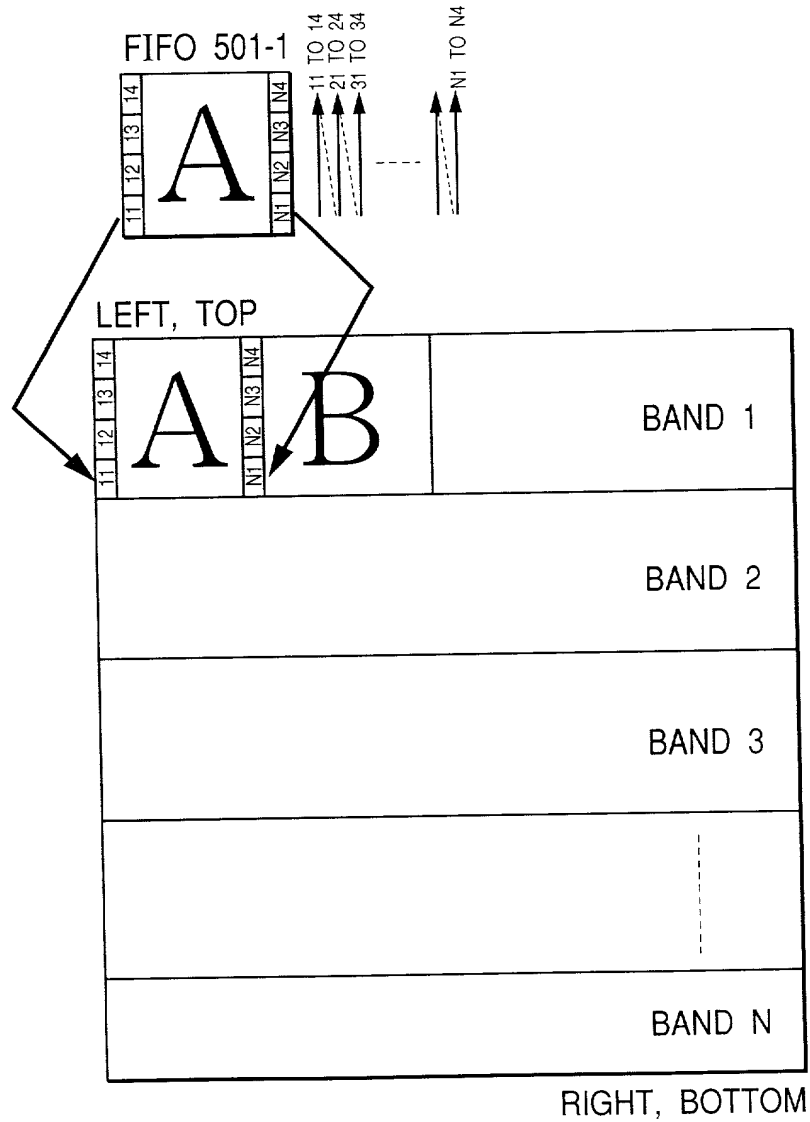


**FIG. 10**



PRINT DATA IN PDL BOARD MEM

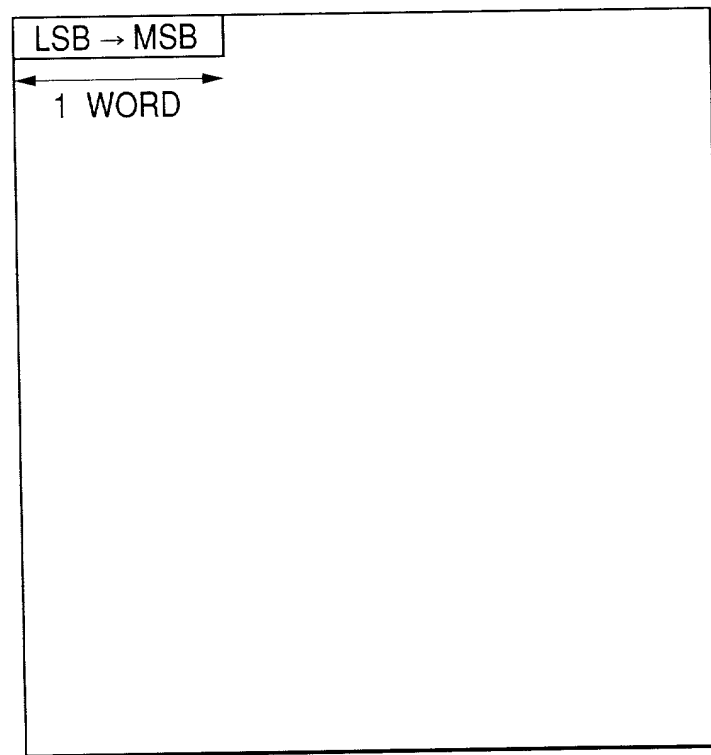
**FIG. 11**



PRINT DATA IN ENGINE MEM

*FIG. 12*

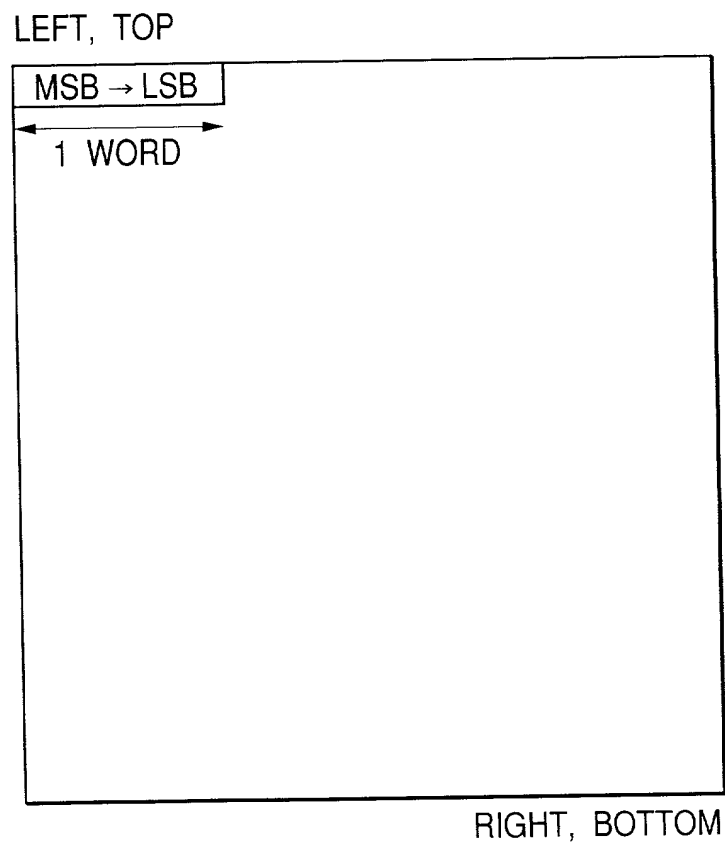
LEFT, TOP



RIGHT, BOTTOM

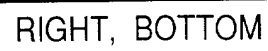
PRINT DATA IN PDL BOARD MEM

*FIG. 13*



PRINT DATA IN ENGINE MEM

LEFT, TOP



PRINT DATA IN PDL BOARD MEM

FIG. 15

LEFT, TOP

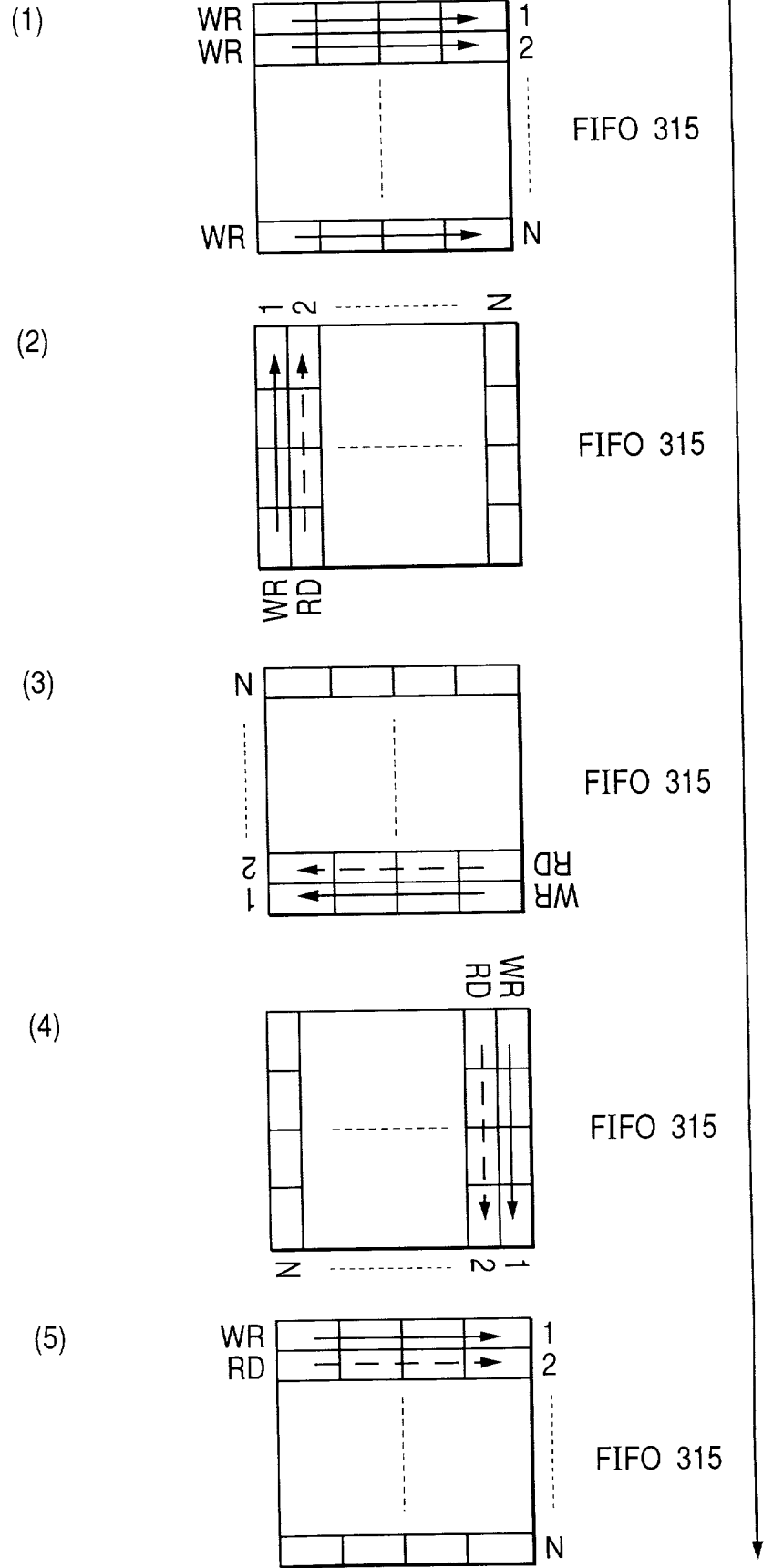
BAND 1
BAND 2
BAND 3
BAND N

RIGHT, BOTTOM

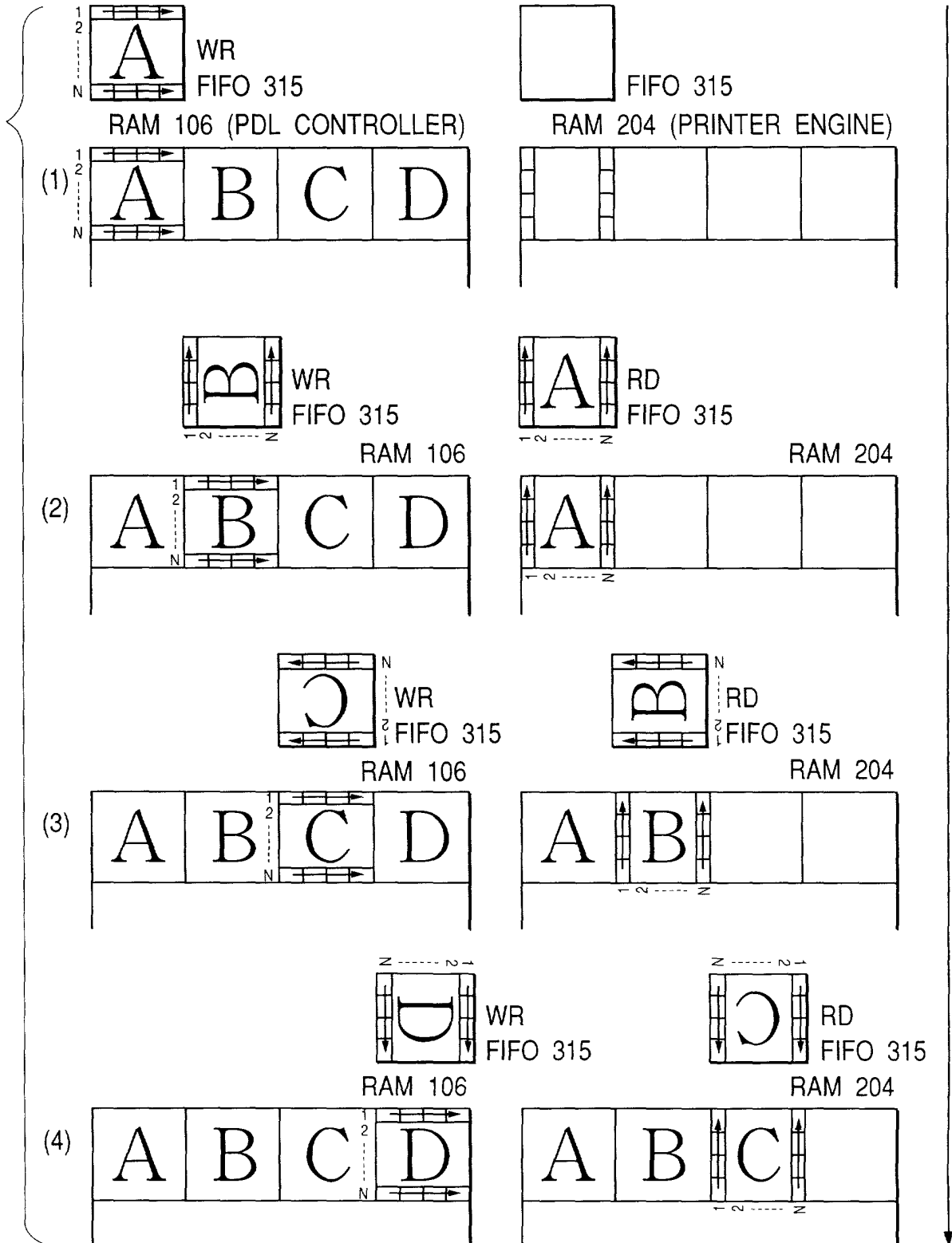
PRINT DATA IN ENGINE MEM

TOP SECRET 6904860

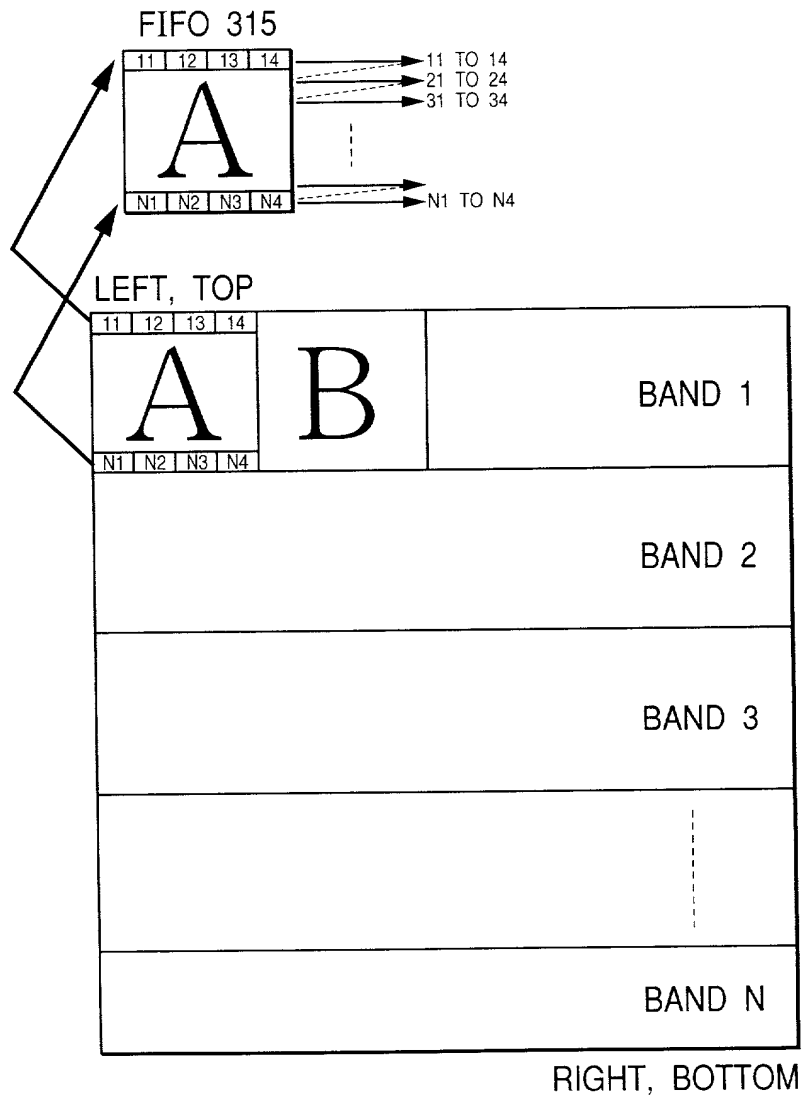
FIG. 16





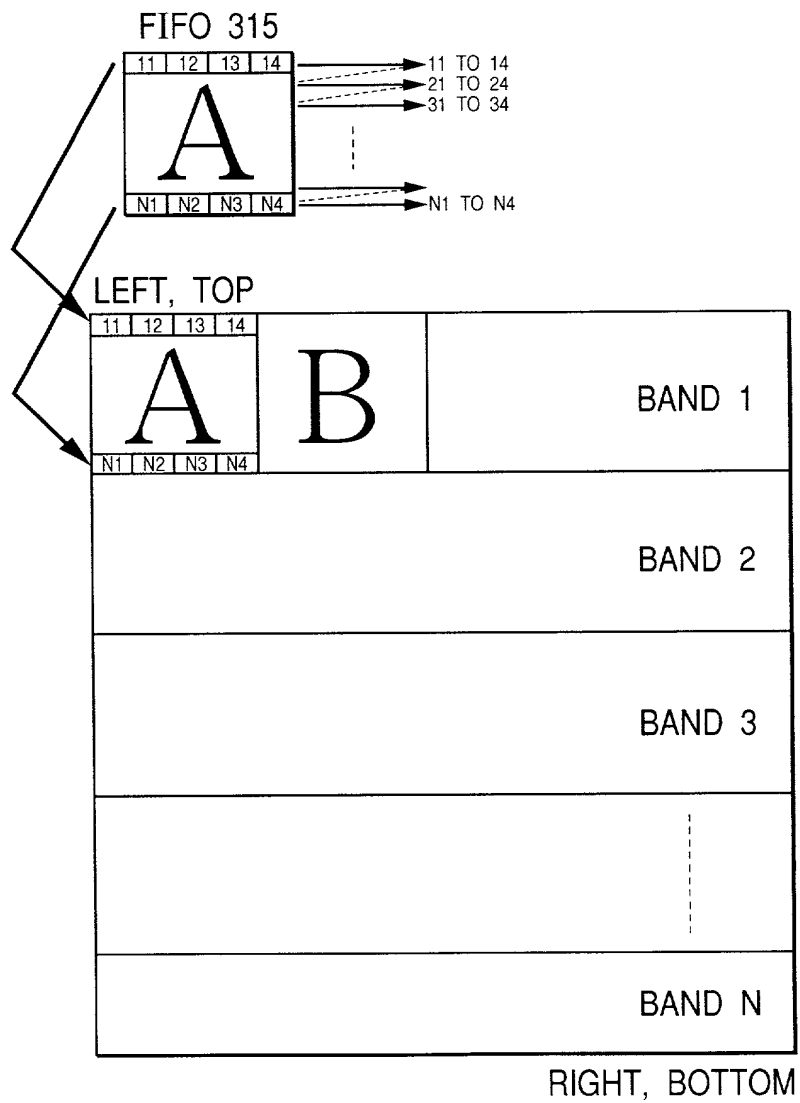
**FIG. 17**

*FIG. 18*



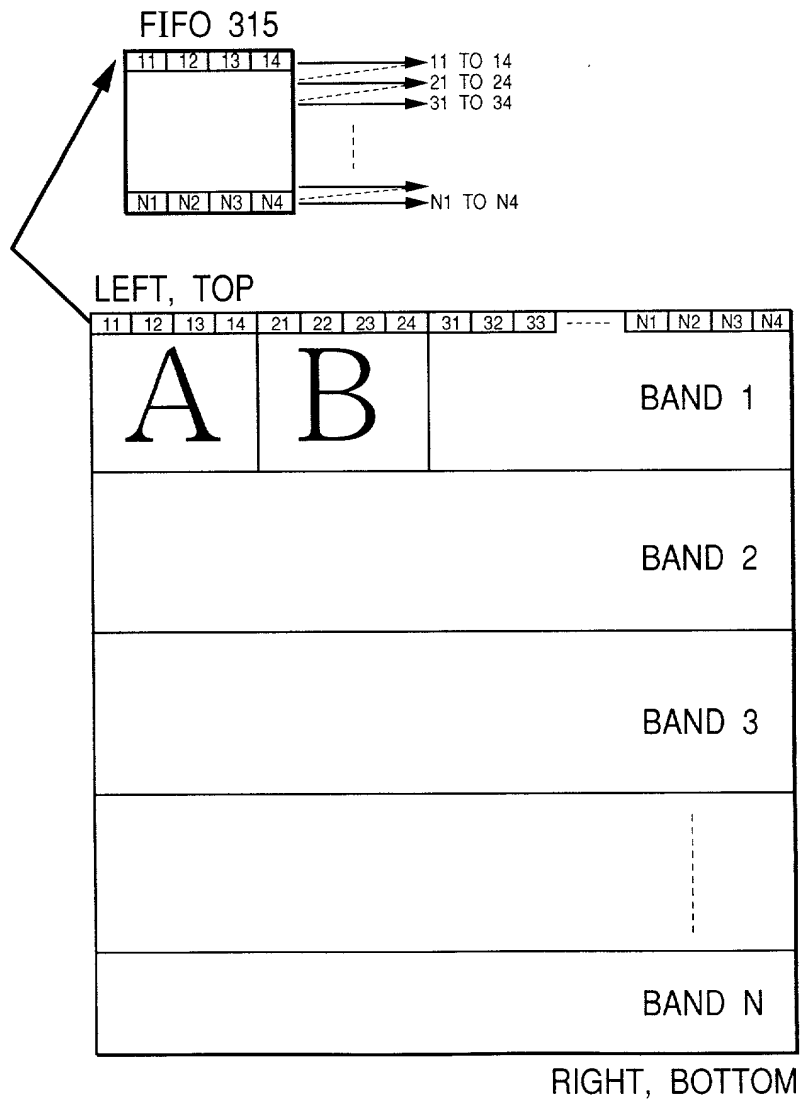
PRINT DATA IN PDL BOARD MEM

*FIG. 19*



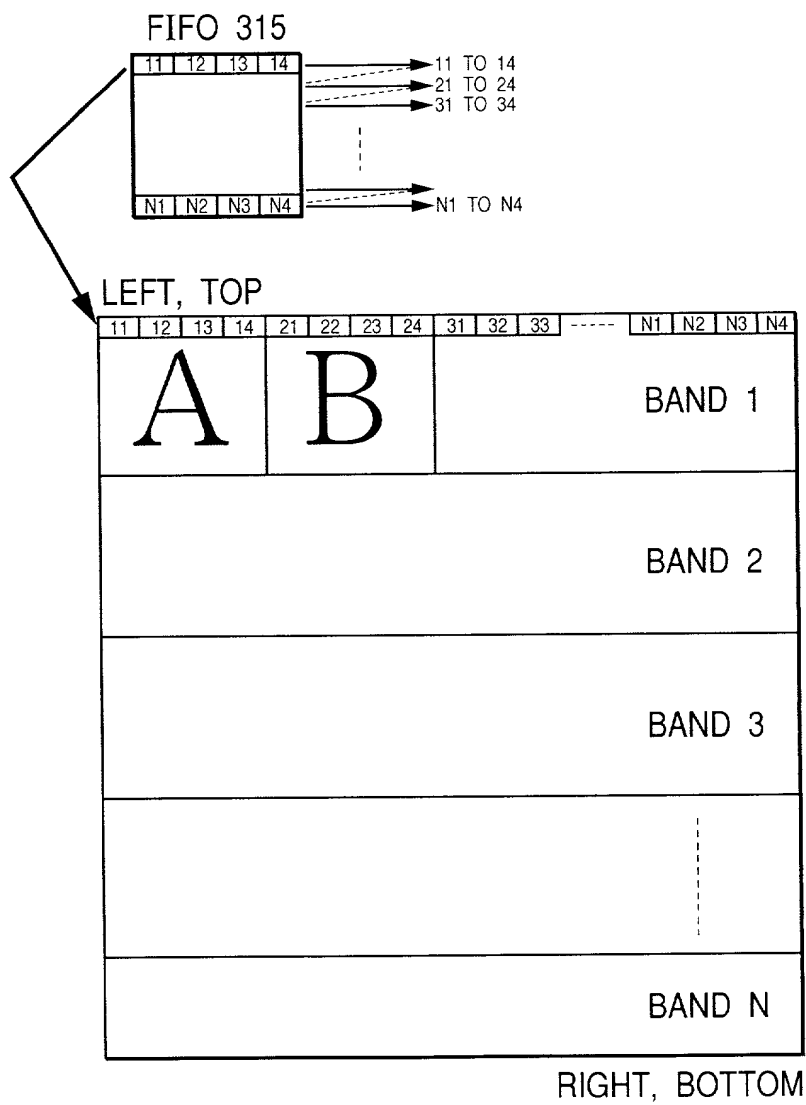
PRINT DATA IN ENGINE MEM

*FIG. 20*



PRINT DATA IN PDL BOARD MEM

FIG. 21



PRINT DATA IN ENGINE MEM

FIG. 22

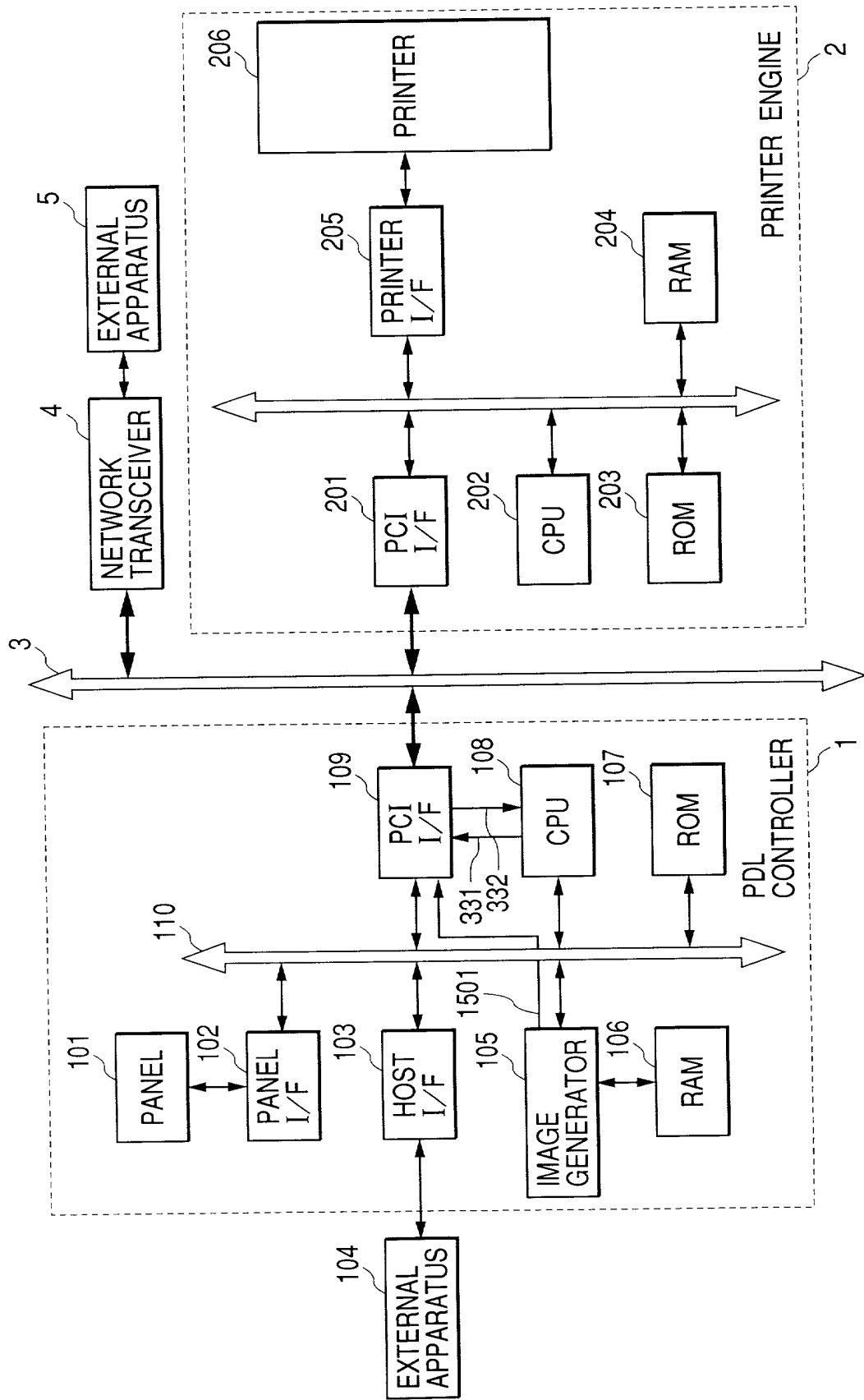


FIG. 23

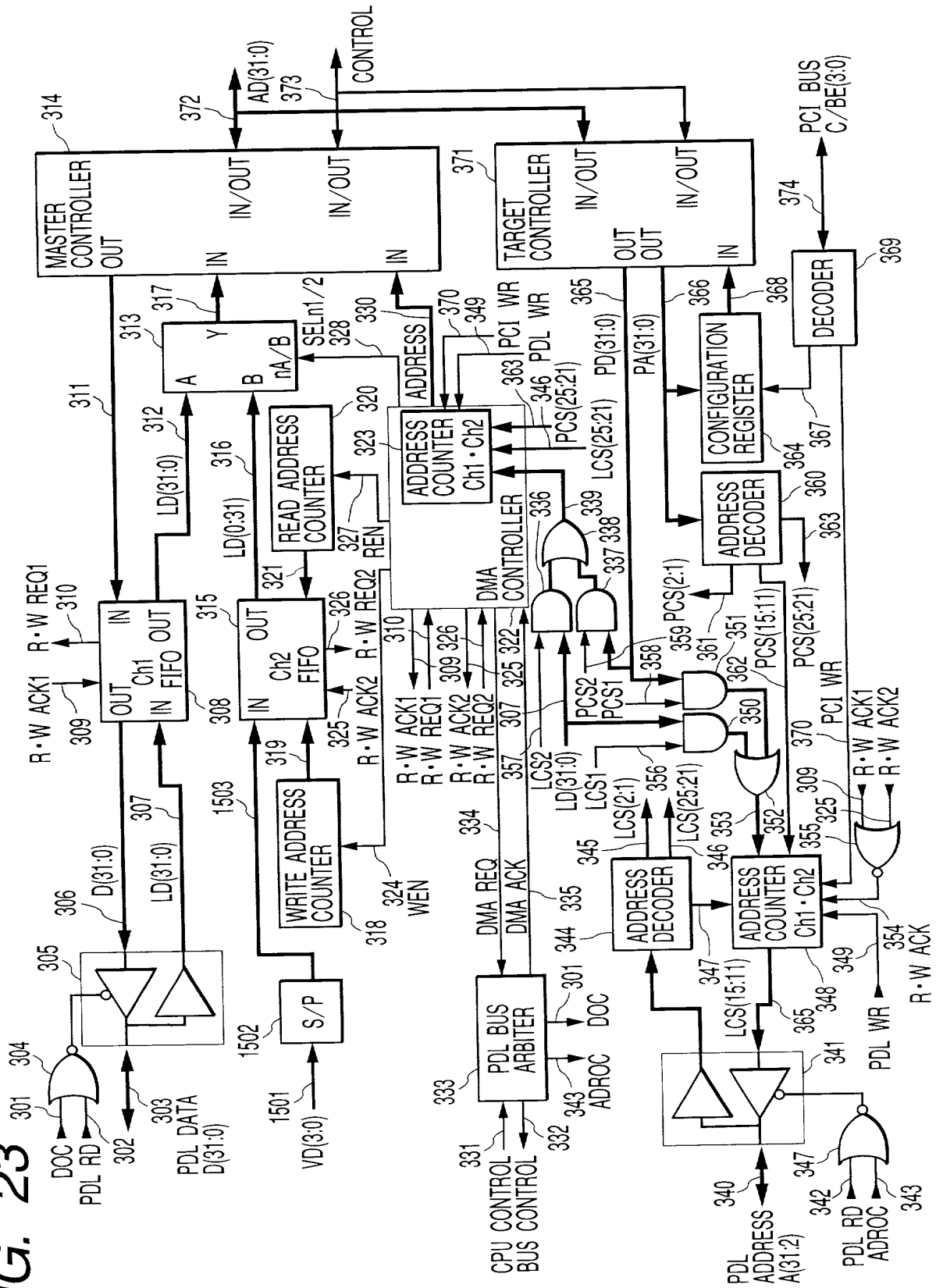


FIG. 24

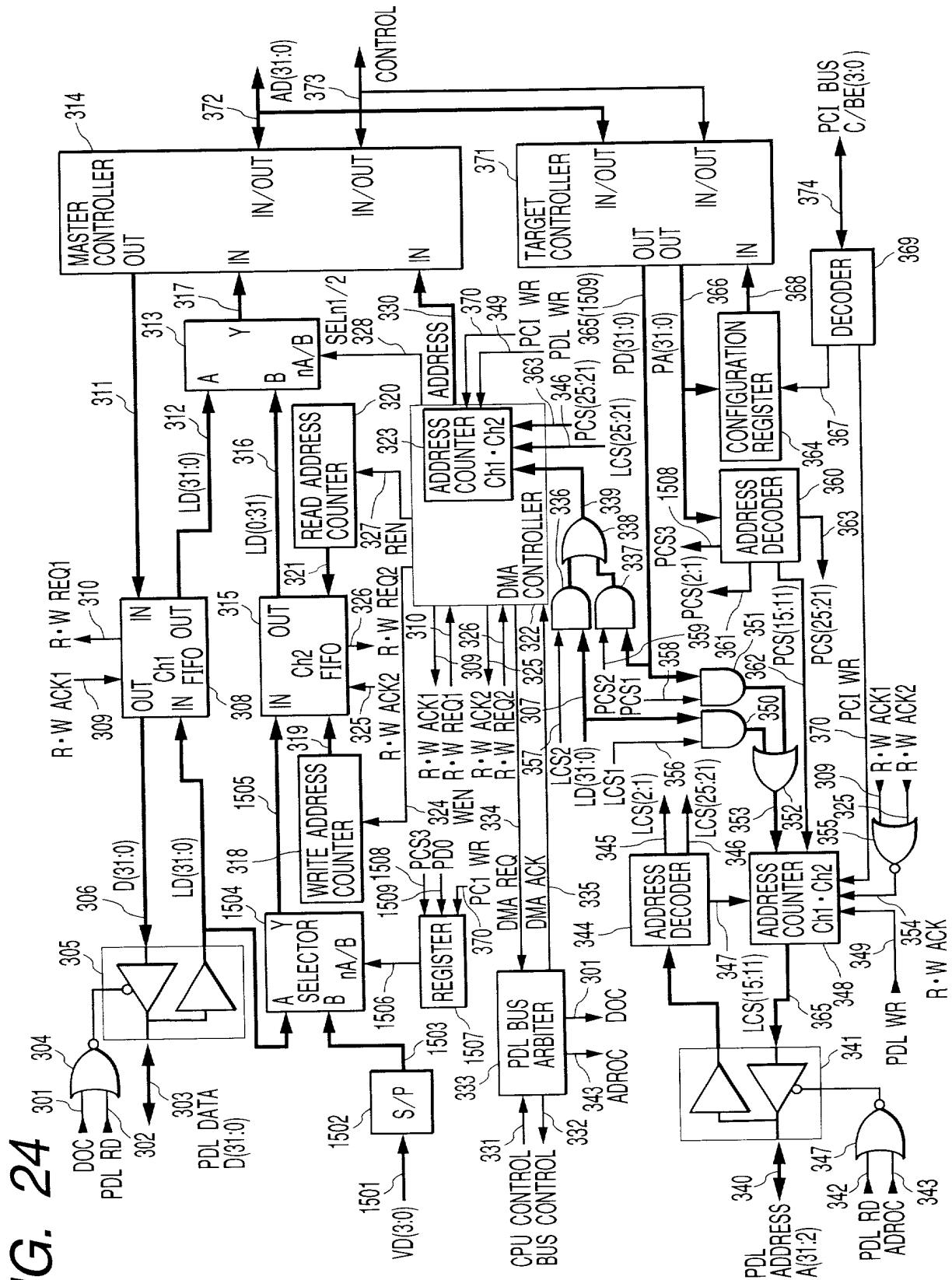




FIG. 25

